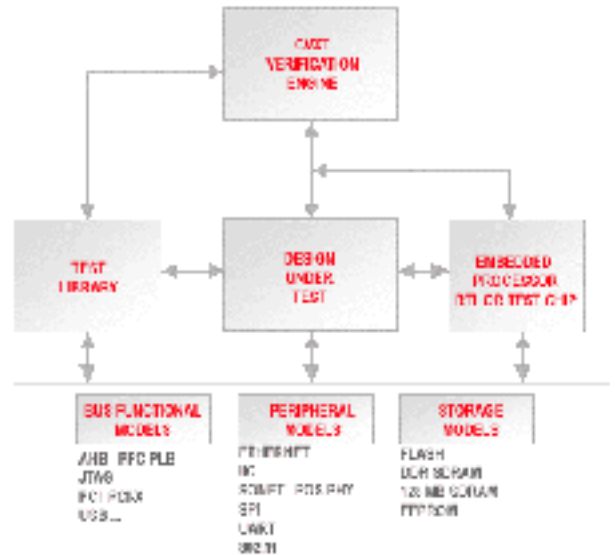


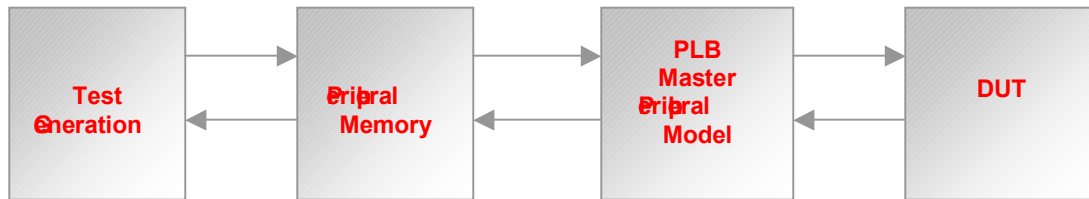
Fiesta[®] B Master Peripheral Model

Features

- Interfaces with Comit Fiesta[®] CVXT Open Verification Environment
- Tests PB Master/Slave & PB Slave devices
- Sideband signals control model behavior to simulate different conditions
- Creates different accesses to the PB Bus based on the contents of Peripheral Memory (See Comit Fiesta[®] CMMT Memory Model)
- PB Access information provided by control DWORD
- Supports line, non-line, burst and non-burst transfers
- Master bus width settable from 32 bits to 256 bits
- Programmable Master Transfer Size and Master Transfer Type
- Implements 4 levels of Master Request Priority
- Supports
 - Master Compressed Data Transfer
 - Master Guarded Memory Access
 - Master Ordered Transfer



System Interface Diagram



Fiesta[®] Process Standardization and Acceleration Tool Kit is an industrial strength suite of tools designed, developed, tested and used by engineers of Comit's Contract Engineering Center. Their experience in developing processes and methodology that yield predictable and accurate results forms the foundation of the toolkit. Use it with confidence.

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